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| Data memory  Instruction memory (from lab 4) program counter (From lab 4)  Pipeline register (ID-EX)  Implement 1st half of instructions for the assembler | اندرو |
| In / out ports  Control unit  Pipeline register (IF - ID)  DO file | ياسر |
| Register file (From lab 4)  Control unit connections  Pipeline register (MEM - WB)  Reset signal  Interconnections | سيف |
| ALU  Stack  Pipeline register (EX - MEM)  Implement 2nd half of instructions for the assembler | عامر |